

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	9345	memory same (amplifier\$3 or amplif\$3) same (read adj3 data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:11
L2	50	1 same (bit\$1line adj3 pair\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 12:24
L3	341	1 same bit\$1line	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 12:24
L4	4	3 same (gate adj3 circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 12:26
L5	84	3 same gate	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:32
L6	75	5 and @ad<"20021231"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:32
L7	69	6 and "365"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:11
L9	183	1 same (gate adj3 circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:11

L10	132	9 and @ad<"20021231"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:11
L11	91	10 and "365"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:12
L12	88	11 not 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:49
L13	3226	amplifier\$3 same (control adj3 gate) same read\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:50
L14	172	13 same bit\$1lin\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:50
L15	3	14 same (read/write adj3 signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:52
L16	5	14 same read/write	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:53
L17	29	14 same (input/output or I/O)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:33

L18	27	17 not 15 not 16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 13:56
L19	82	13 and 365/206,207,208.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:32
L20	576	1 and 365/206,207,208.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:32
L21	523	20 and @ad<"20021231"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:32
L22	268	21 and (input/output or I/O)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:33
L23	233	22 and read and write	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:33
L25	60	23 and (control adj3 gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 14:34
L26	45	25 not 19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 15:20

L27	0	(circuit and amplifier\$3 and (read adj3 data) and (bit\$1in\$1 adj2 pair\$1) and (read adj3 command) and (gate adj3 circuit) and (read/write adj2 signal)).clms.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 15:24
L28	5	lee-seung.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 15:25
L29	0	sim-jae.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/30 15:25